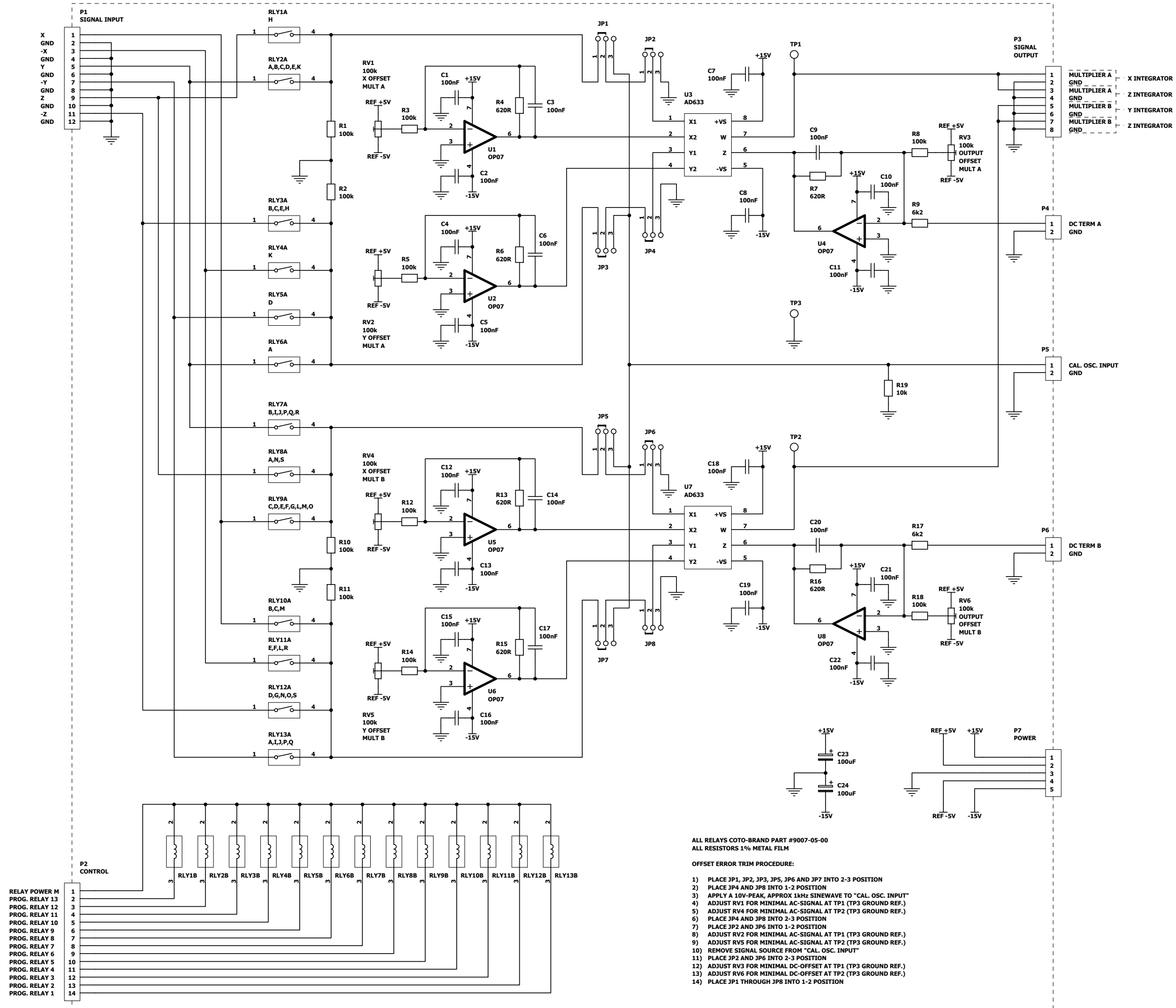


SELF-PROGRAMMING ANALOG COMPUTER FOR CHAOTIC SPROTT SYSTEMS

MULTIPLIER BOARD
GLEN KLEINSCHMIDT JAN 2016



ALL RELAYS COTO-BRAND PART #9007-05-00
ALL RESISTORS 1% METAL FILM

OFFSET ERROR TRIM PROCEDURE:

- 1) PLACE JP1, JP2, JP3, JP5, JP6 AND JP7 INTO 2-3 POSITION
- 2) PLACE JP4 AND JP8 INTO 1-2 POSITION
- 3) APPLY A 10V-PEAK, APPROX 1kHz SINWAVE TO "CAL. OSC. INPUT"
- 4) ADJUST RV1 FOR MINIMAL AC-SIGNAL AT TP1 (TP3 GROUND REF.)
- 5) ADJUST RV4 FOR MINIMAL AC-SIGNAL AT TP2 (TP3 GROUND REF.)
- 6) PLACE JP4 AND JP8 INTO 2-3 POSITION
- 7) PLACE JP2 AND JP6 INTO 1-2 POSITION
- 8) ADJUST RV2 FOR MINIMAL AC-SIGNAL AT TP1 (TP3 GROUND REF.)
- 9) ADJUST RV5 FOR MINIMAL AC-SIGNAL AT TP2 (TP3 GROUND REF.)
- 10) REMOVE SIGNAL SOURCE FROM "CAL. OSC. INPUT"
- 11) PLACE JP2 AND JP6 INTO 2-3 POSITION
- 12) ADJUST RV3 FOR MINIMAL DC-OFFSET AT TP1 (TP3 GROUND REF.)
- 13) ADJUST RV6 FOR MINIMAL DC-OFFSET AT TP2 (TP3 GROUND REF.)
- 14) PLACE JP1 THROUGH JP8 INTO 1-2 POSITION